

| | L # | Hits | Search Text | DBs | Time Stamp |
|----|-----|------|--|-------|------------------|
| 1 | L1 | 864 | 327/51.ccls. or 327/52.ccls. or 327/54.ccls. or 327/55.ccls. | USPAT | 2000/09/05 10:19 |
| 2 | L2 | 904 | ((((327/56.CCLS.) OR (327/198.CCLS.)) OR (327/199.CCLS.)) OR (327/210.CCLS.)) | USPAT | 2000/09/05 10:21 |
| 3 | L3 | 555 | ((((327/211.CCLS.) OR (327/212.CCLS.)) OR (327/215.CCLS.)) OR (327/219.CCLS.)) | USPAT | 2000/09/05 10:23 |
| 4 | L4 | 185 | (327/287.CCLS.) OR (327/299.CCLS.) | USPAT | 2000/09/05 10:23 |
| 5 | L5 | 2355 | 1 2 3 4 | USPAT | 2000/09/05 10:23 |
| 6 | L6 | 1116 | 5 and (latch or latching or flip-flop) | USPAT | 2000/09/05 10:24 |
| 7 | L7 | 972 | 6 and (transistors or CMOS) | USPAT | 2000/09/05 10:24 |
| 8 | L8 | 588 | 7 and inverter | USPAT | 2000/09/05 10:24 |
| 9 | L9 | 282 | 8 and ((control or enable) adj signal) | USPAT | 2000/09/05 13:29 |
| 10 | L10 | 156 | driver and (series near2 inverters) and transistor and 327/\$.ccls. | USPAT | 2000/09/05 13:43 |
| 11 | L11 | 1279 | 327/108.ccls. or 327/109.ccls. or 327/112.ccls. | USPAT | 2000/09/05 13:44 |
| 12 | L12 | 262 | 11 and inverters and transistor | USPAT | 2000/09/05 13:45 |